

An Improved Shading Cache for Modern GPUs

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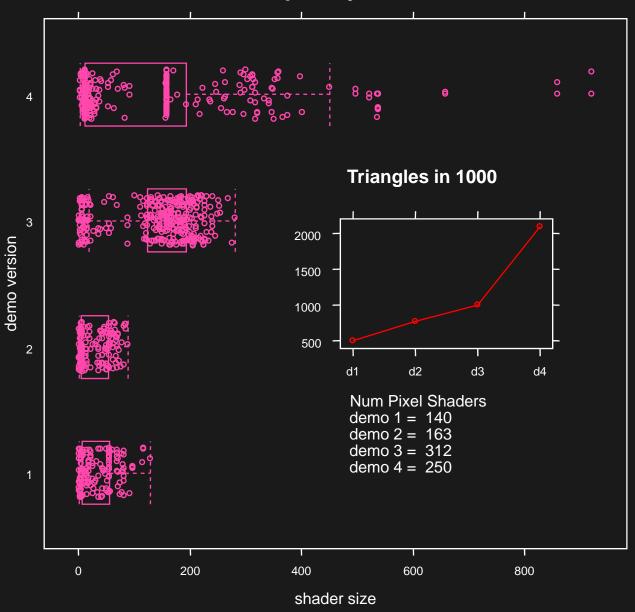


Motivation





Shader Complexity of ATI demos



Related Work: Code Simplification

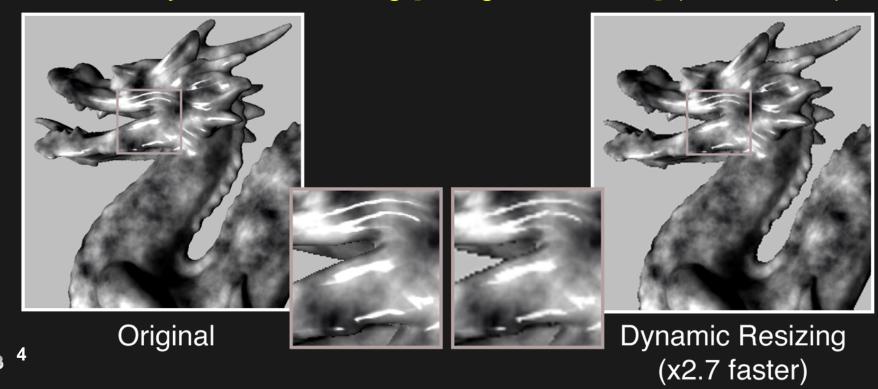
- Replace subexpressions with constants
- Automatic shader level of detail [Olano et al. 2003]
- User-configurable automatic simplification [Pellacini 2005]





Related Work: Dynamic Resizing

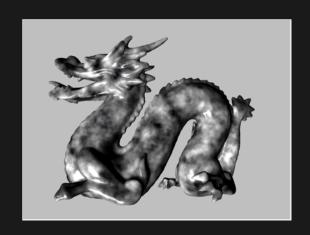
- Render scene to lo-res off-screen buffer and upsample to target resolution
- InfiniteReality system [Montrym et al. 1997]
- Geometry-Aware resizing [Yang et al. 2008] (concurrent)



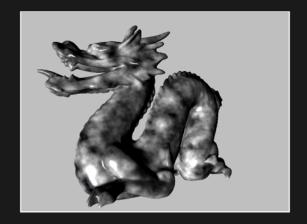


Related Work: Temporal Reprojection

- Reuse partial shading calculations across consecutive frames
- Reverse reprojection cache [Nehab et al. 2007]
- Pixel-correct shadow maps with temporal reprojection and shadow test confidence [Scherzer et al. 2007]
- Multi-view architecture [Hasselgren et al. 2006]



frame n-1



frame n



green = mutually visible red = occluded



Frame n-1



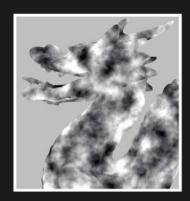
framebuffer



Frame n-1







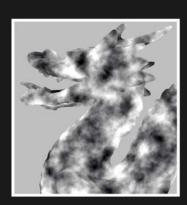
payload



Frame n-1



framebuffer

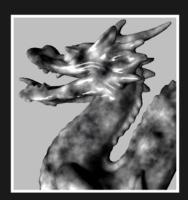


payload

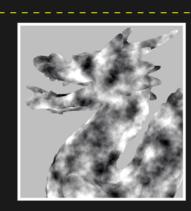


depth

Frame n-1



framebuffer



payload

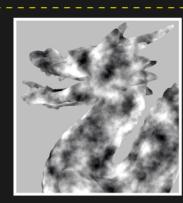


depth

Frame n-1



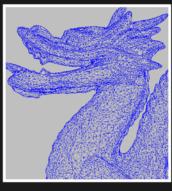
framebuffer



payload

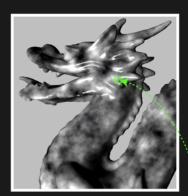


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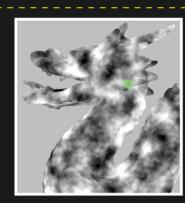


framebuffer

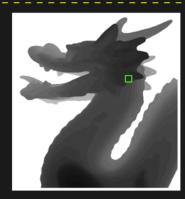
Frame n-1



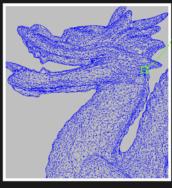
framebuffer



payload

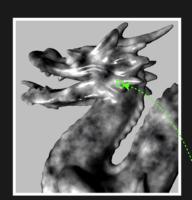


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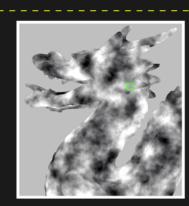


framebuffer

Frame n-1



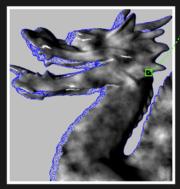
framebuffer



payload

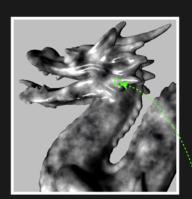


depth

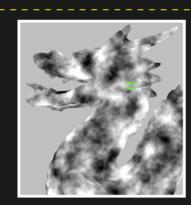


framebuffer

Frame n-1



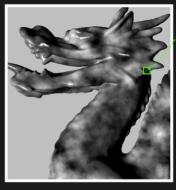
framebuffer



payload

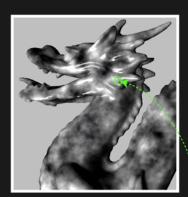


depth

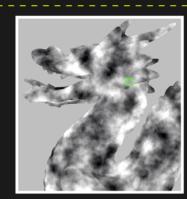


framebuffer

Frame n-1



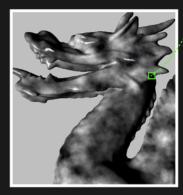
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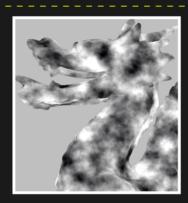
payload



depth



framebuffer



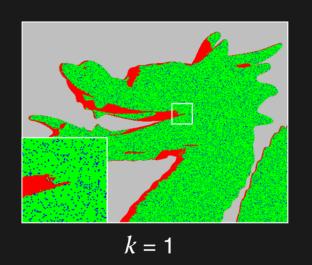
payload

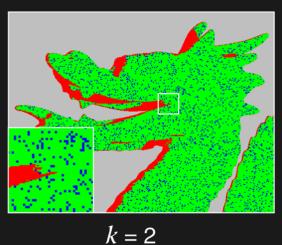


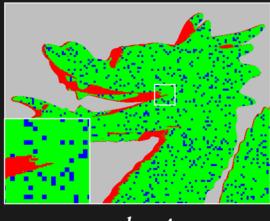
depth

Cache Refresh

- Scene points may remain visible over many frames
- Cached entries will become stale due to changes in shader inputs and from resampling error
- Explicitly refresh cached entries within a user-set refresh period
 ∆n by forcing misses within k x k blocks of pixels

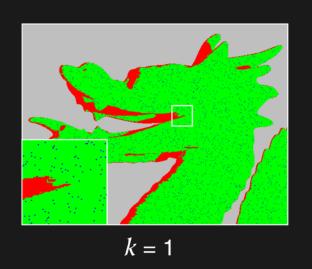


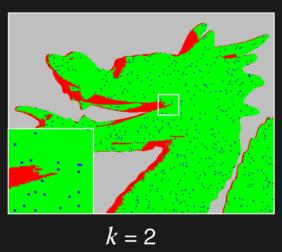


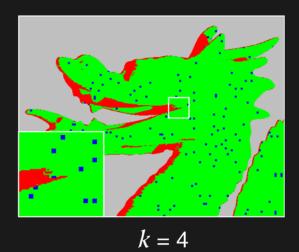


Cache Refresh

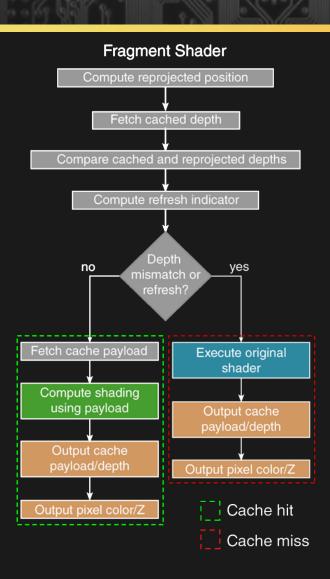
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 ∆n by forcing misses within k x k blocks of pixels





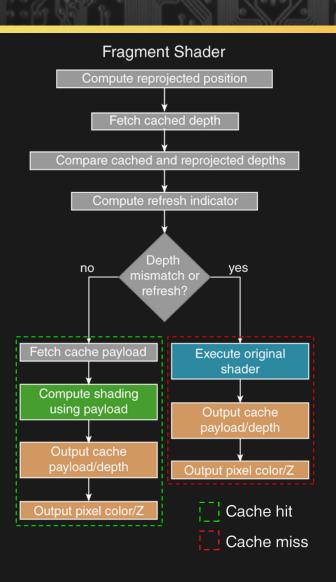


1-Pass Algorithm [Nehab et al. 2007]



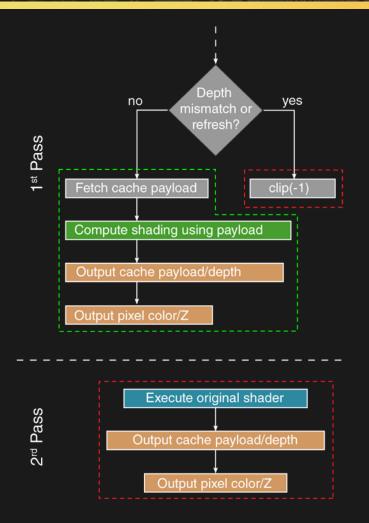


1-Pass Algorithm [Nehab et al. 2007]

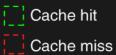


- Branch efficiency of underlying hardware
- Relative cost of processing hit and miss
- Use of multiple render targets (MRTs)

2-Pass Algorithm [Nehab et al. 2007]

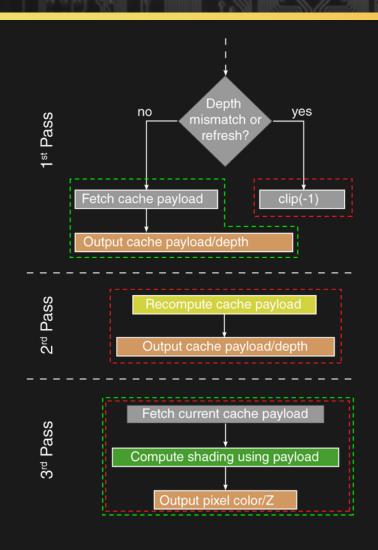


- Still depends on branch efficiency; however, difference in cost of paths is reduced when hit << miss
- Still requires MRTs

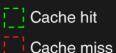




3-Pass Algorithm (Our approach)

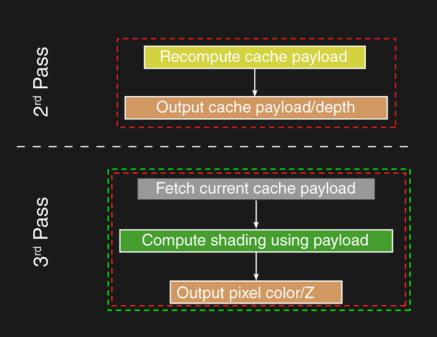


- Execution paths in the first pass are independent of what is being cached
- Not require MRTs
- Drawback three rendering passes

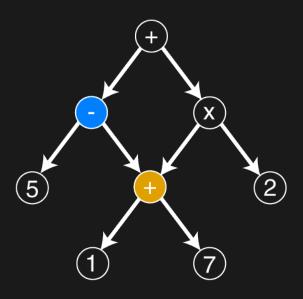




Computation Overlap Problem



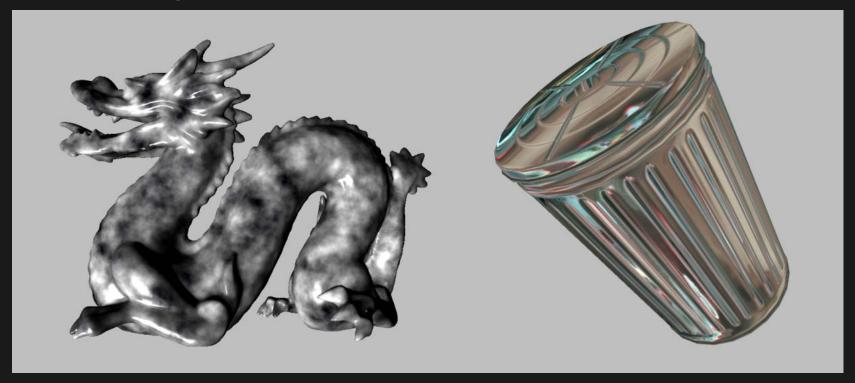
+80+4.:,,.+5×+4.:,,



Test Scenes

Dragon shader

Trashcan shader



procedural noise with Blinn-Phong specular layer (75K triangles) supersampled (25) environment map (15K triangles)

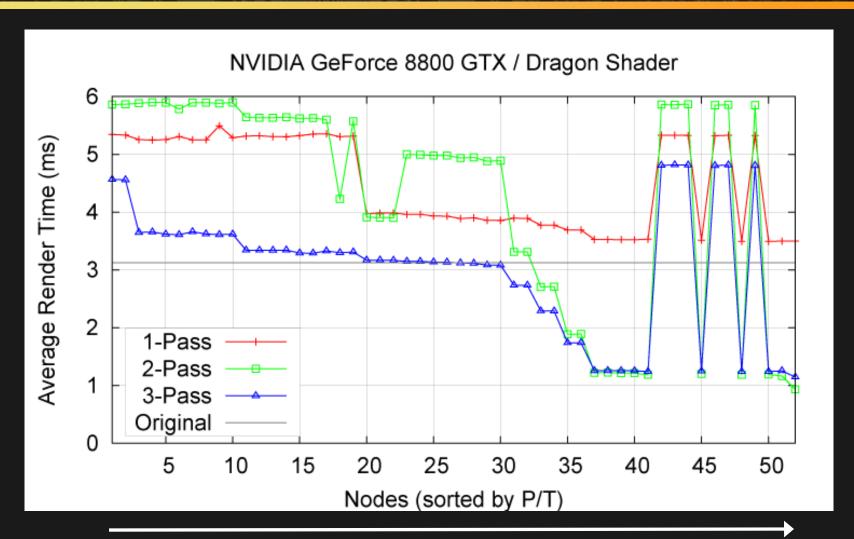


Experiment #1

- Generated versions of the shader that caches every intermediate calculation
- Compute cost of evaluating payload (P)
- Compute cost of evaluating full shader (T)
- Fixed refresh period of 32 and 4 x 4 block size
- Compare performance of three different algorithms on NVIDIA Geforce 8800GTX and ATI Radeon 2900TX

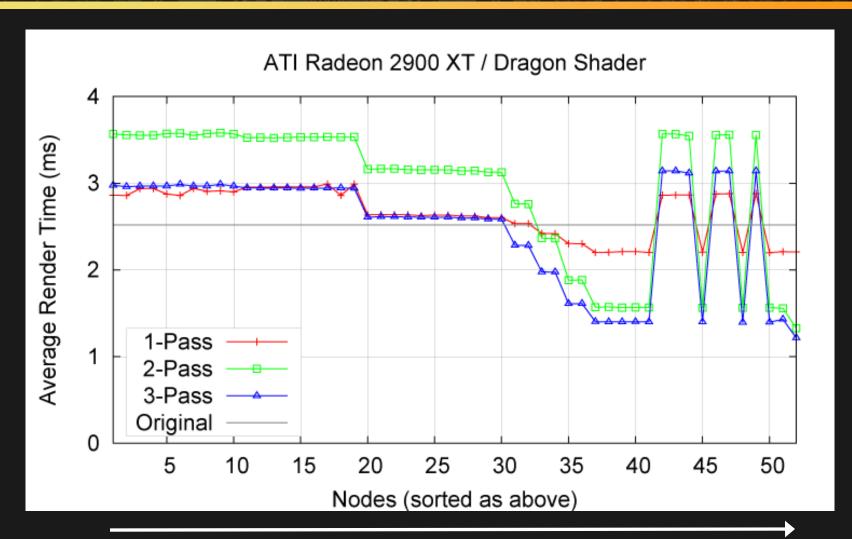


Experiment #1: Dragon / NVIDIA



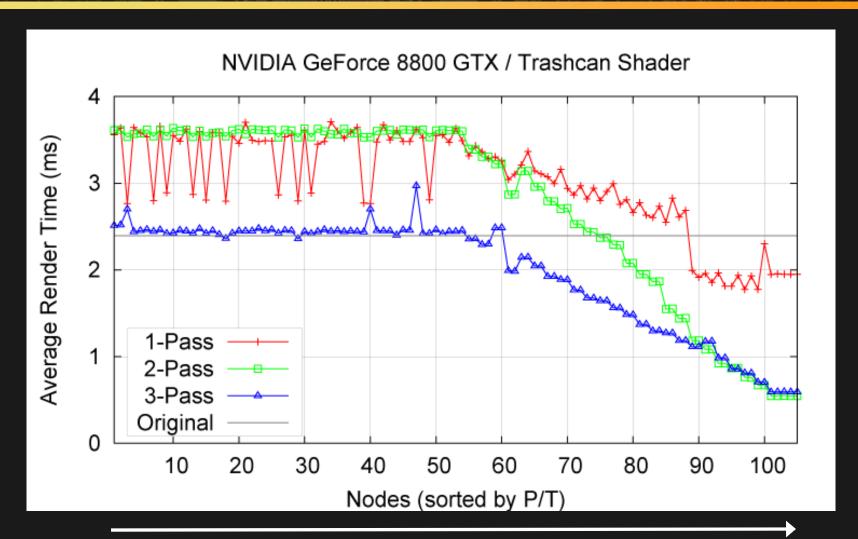


Experiment #1: Dragon / ATI



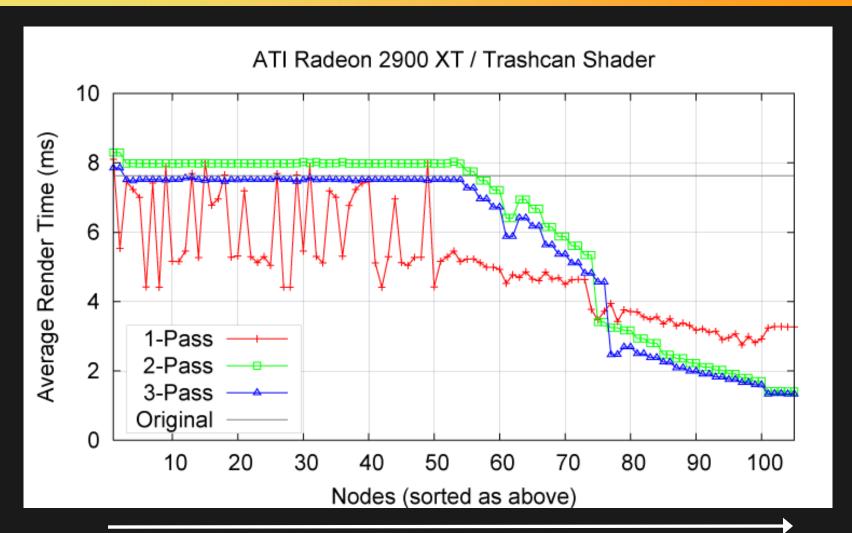


Experiment #1: Trashcan / NVIDIA



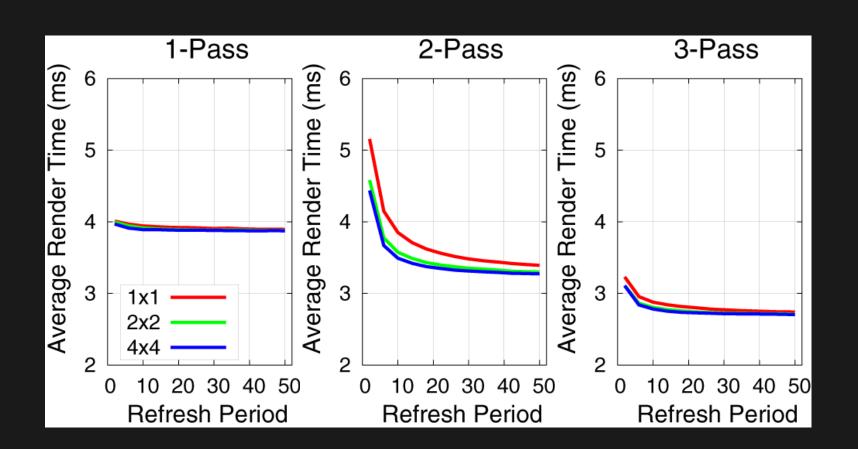


Experiment #1: Trashcan / ATI





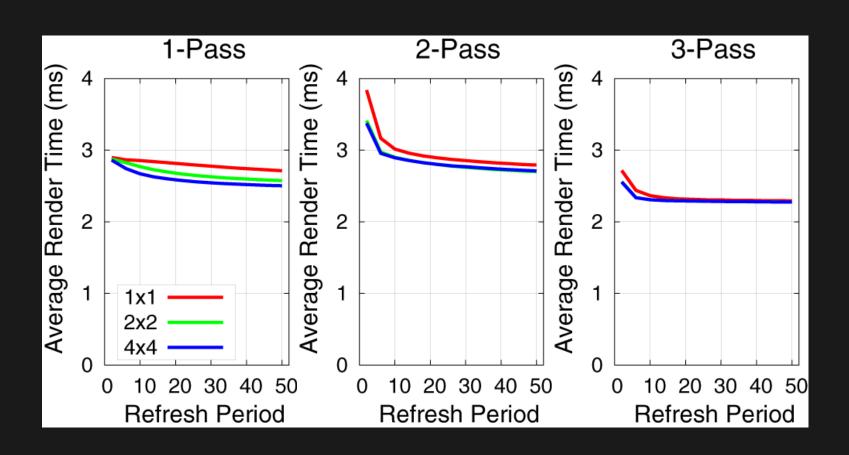
Experiment #2: Refresh parameters



Dragon Shader: NVIDIA Geforce 8800GTX



Experiment #2: Refresh parameters



Dragon Shader: ATI 2900TX



Conclusion

- Introduced an improved implementation of a shading reprojection cache
- Require single target and limits reliance on efficient branching in hardware
- More consistent performance across a wide range of cache loads on modern NVIDIA and ATI hardware



Future Work

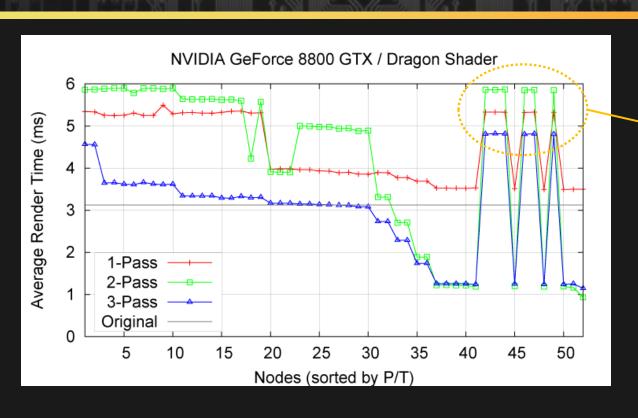
- Explore the possibility of combing existing acceleration techniques
- Automatic cache allocation
- Alternative cache parameterization



Thank You



Elaboration on Experiment #1 Results



 α noise()+(1- α)noise()

Imagine caching α noise() subexpression, noise() would need to be called in both hit and miss paths.

